Appl. No.: 10/720,701
Amdt. dated February 6, 2008
Reply to Office Action of September 10, 2007

Amendments to the Specification:

Please replace paragraph [0070] with:

[0070] In detail, the IDFT unit 210 comprises a mapper 110, serial to parallel converters 121 through [[12*m*]] **120m**, a parallel to serial converter and guard interval inserter 140, and an interleaver 211 including MxN memory buffers provided between first and second IDFT units 212 and 213.

Please replace paragraph [0071] with:

[0071] When receiving mapped MQAM symbols from the mapper **110** of the transmitter I, the serial to parallel converters **121** through [[12*m*]] **120m** sequentially receive the mapped MQAM symbols, and convert them into vector data. Respective m IDFT units of the first N-point IDFT unit **212** receive the vector data, perform IDFT on them, and transmit respective output values to the interleaver **211**.

OK TO ENTER: /DM/